



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Response Under 37 CFR 1.116 - Expedited Procedure

Customer Number: 20277

Kenji YONEDA

Confirmation Number: 9901

Application No.: 10/774,522

Group Art Unit: 2891

Filed: February 10, 2004

Examiner: SARKAR, Asok K.

For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE

Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	23	23	0	\$50.00 =	\$0.00
Independent Claims	5	5	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL &amp; EMERY LLP

Michael E. Fogarty  
 Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
 Washington, DC 20005-3096  
 Phone: 202.756.8000 MEF:jrj  
 Facsimile: 202.756.8087  
 Date: March 1, 2006

Please recognize our Customer No. 20277 as our  
 correspondence address.

AF  
 JFW



Docket No.: 060188-0768

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	<b>Response under 37 C.F.R. § 1.116</b>
	:	<b><u>Expedited Procedure</u></b>
Kenji YONEDA	:	
	:	Customer Number: 20277
Application No.: 10/774,522	:	Confirmation Number: 9901
	:	
Filed: February 10, 2004	:	Group Art Unit: 2891
	:	Examiner: SARKAR, Asok K.

For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR  
DEVICE

**AMENDMENT UNDER 37 C.F.R. § 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 1, 2005, having a three-month shortened statutory period for reply set to expire on March 1, 2006, please amend the above identified patent application as follows.